

2STR2215

Low voltage fast-switching PNP power transistor

Features

- Very low collector-emitter saturation voltage
- High current gain characteristic
- Fast switching speed
- Miniature SOT-23 plastic package for surface mounting circuits

Applications

- LED
- Battery charger
- Motor and relay driver
- Voltage regulation

Description

The 2STR2215 is a PNP transistor manufactured using new "PB-HCD" (Power Bipolar High Current Density) technology. The resulting transistor shows exceptional high gain performances coupled with very low saturation voltage. The complementary NPN is the 2STR1215.

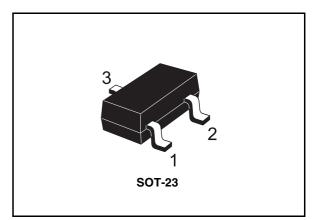


Figure 1. Internal schematic diagram

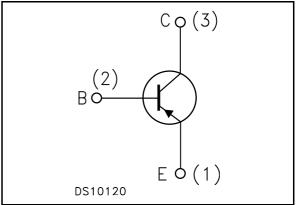


Table 1. Device summary

Order code	Marking	Package	Packaging
2STR2215	215	SOT-23	Tape and reel

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Electrical ratings

Table 2.Absolute maximum rating	Table 2.	Absolute	maximum	rating
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Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	-15	V
V _{CEO}	Collector-emitter voltage $(I_B = 0)$	-15	V
V _{EBO}	Emitter-base voltage (I _C = 0)	-5	V
۱ _C	Collector current	-1.5	А
I _{CM}	Collector peak current (t _P < 5 ms)	-3	А
P _{tot}	Total dissipation at T _{amb} = 25 °C	0.5	W
T _{stg}	Storage temperature	-65 to 150	°C
Т _Ј	Max. operating junction temperature	150	°C

Table 3.Thermal data

Symbol	Parameter	Value	Unit
R _{thj-amb} ⁽¹⁾	Thermal resistance junction-amb max	250	°C/W

(1) Device mounted on PCB area of 1 cm^2



2 Electrical characteristics

(T_{case} = 25 °C unless otherwise specified)

Table 4.	Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E =0)	$V_{CD} = -15 V$			-0.1	μA
I _{EBO}	Emitter cut-off current (I _C =0)	V _{EB} = -4 V			-0.1	μA
V _{(BR)CBO}	Collector-base breakdown voltage $I_{C} = -100 \ \mu A$ $(I_{E} = 0)$		-15			V
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	I _C = -10 mA	-15			V
$V_{(BR)EBO}$ Emitter-base breakdown voltage (I _C = 0)		I _E = -100 μA	-5			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$ I_{C} = -100 \text{ mA } I_{B} = -1 \text{ mA} $ $ I_{C} = -1 \text{ A} \qquad I_{B} = -100 \text{ mA} $ $ I_{C} = -2 \text{ A} \qquad I_{B} = -200 \text{ mA} $		-0.25 -0.40	-0.15 -0.50 -0.85	V V V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = -1 A I _B = -100 mA		-0.90	-1.25	V
h _{FE} ⁽¹⁾	DC current gain	$ I_{C} = -50 \text{ mA} V_{CE} = -2 \text{ V} \\ I_{C} = -500 \text{ mA} V_{CE} = -2 \text{ V} \\ I_{C} = -1 \text{ A} \qquad V_{CE} = -2 \text{ V} \\ I_{C} = -2 \text{ A} \qquad V_{CE} = -2 \text{ V} $	200 200 130 80	280	560	
C_{CBO} Collector-base capacitance ($I_E = 0$)		V _{CB} = -10 V f = 1 MHz		20		pF
tonResistive loadtonTurn-on timetoffTurn-off time		$I_{C} = -1.5 \text{ A}$ $V_{CC} = -10 \text{ V}$ $I_{B1} = -I_{B2} = -150 \text{ mA}$		60 220		ns ns

1. Pulsed duration = 300 μ s, duty cycle \leq 1.5%



Electrical characteristics (curves) 2.1

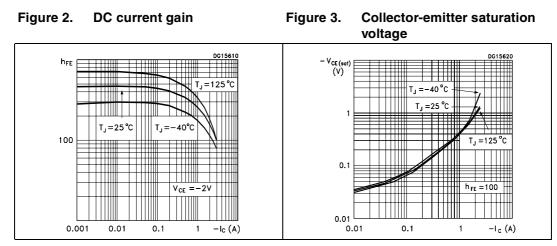


Figure 4. **Base-emitter saturation** voltage

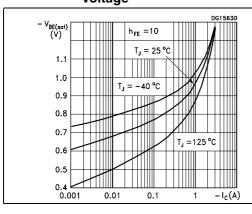
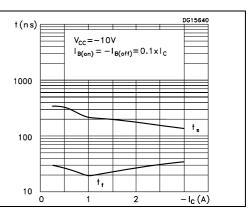
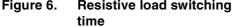


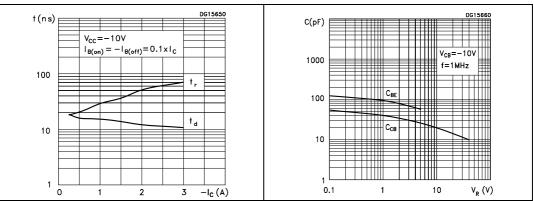
Figure 6. **Resistive load switching**











2.2 Test circuit

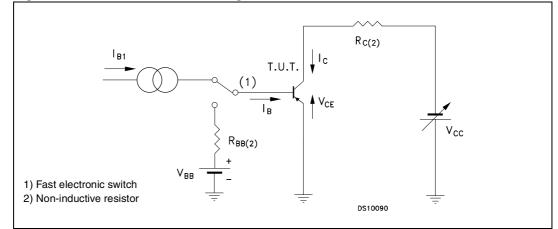


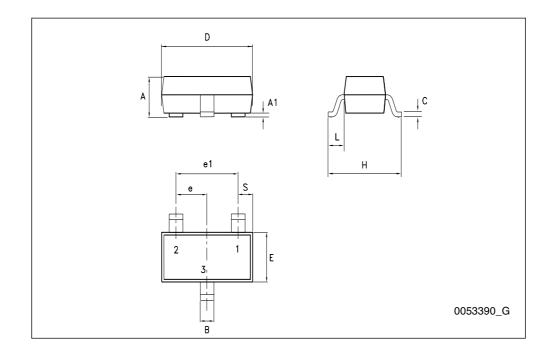
Figure 8. Resistive load switching test circuit

3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



SOT-23 mechanical data				
DIM.	mm.			
DIM.	min.	typ	max.	
А	0.89		1.4	
A1	0		0.1	
В	0.3		0.51	
С	0.085		0.18	
D	2.75		3.04	
е	0.85		1.05	
e1	1.7		2.1	
E	1.2		1.6	
н	2.1		2.75	
L		0.6		
S	0.35		0.65	





4 Revision history

Table 5.Document revision history

Date	Revision	Changes
09-Feb-2006	1	Initial release.
20-Jul-2006	2	New template.
08-Sep-2008	3	Updated the SOT-23 mechanical data.



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